

Inverse Modeling Estimation Algorithm for Multi-Carrier High Power Amplifiers

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This article describes the mathematical basis of a technique for digital pre-distortion using periodic rather than real-time sampling of the system output.

Many predistortion schemes use a memory-less approach [1, 2], but with broadband systems, where the bandwidth is typically larger than five MHz, the PA typically

can no longer be considered a memory-less system. With this, the inverse transfer function of the PA—which describes the forward transfer function of the required pre-distorter function—can no longer be obtained using memory-less approaches. The reason for this is that, for any given input envelope value, there are an infinite number of possible values for the inverse function.

To overcome this, the PA output and the pre-distorter can be modeled as the summation of a number of non-linear P^{th} order polynomials operating on the current sample, as well as other past input samples [3]. For the PA, this is given by,

$$y(n) = \sum_{m=0}^{m=M} x(n-m) \sum_{k=1}^{k=P} b_{mk} |x(n-m)|^{k-1} \quad (1)$$

where the value of M is the memory depth, or number of taps, P is the polynomial order, and x and y are the input and output baseband samples of the PA, respectively. When memory is negligible, that is $M = 0$, the model (1) reduces to the simple memory-less PA polynomial model given by,

$$y(n) = x(n) \sum_{k=1}^{k=P} b_k |x(n)|^{k-1}$$

or,

$$x(n) \left[b_1 + b_2 |x(n)| + b_3 |x(n)|^2 + \dots b_p |x(n)|^{p-1} \right]$$

To perform pre-distortion, using a pre-distorter non-linear memory based model, the following is used,

$$x_{PD}(n) = \sum_{m=0}^{m=L} x(n-m) \sum_{k=1}^{k=K} a_{mk} |x(n-m)|^{k-1} \quad (2)$$

where $x_{PD}(n)$ is the pre-distorter output, L is the memory, and K is the polynomial order. Notice here that the pre-distorter Memory depth and polynomial order practically can not be equal to M and P in eq. (1), however, their values are chosen such that the inverse function of eq. (1) implemented in the pre-distorter is a good approximation, which is limited by the programmable circuits used to implement eq. (2) in the digital domain.

Offline PA Inverse Model Estimation

Figure 1 illustrates the hardware implementation of the principle of indirect linearization based on eqs. (1) and (2), or what is also known as the *offline modeling technique* that is used for extracting the pre-distorter coefficients a_{mk} in eq. (2) which represent the PA inverse transfer function [4, 5].

Here, the inverse modeling is done by feeding the PA baseband output complex samples (not input) as inputs to the offline model eq. (2) that is forced iteratively to mimic the inverse of the PA non-linear and memory behaviour model of eq. (1). Due to the fact that the PA dynamics change very slowly over time, this process is not required to be implemented in

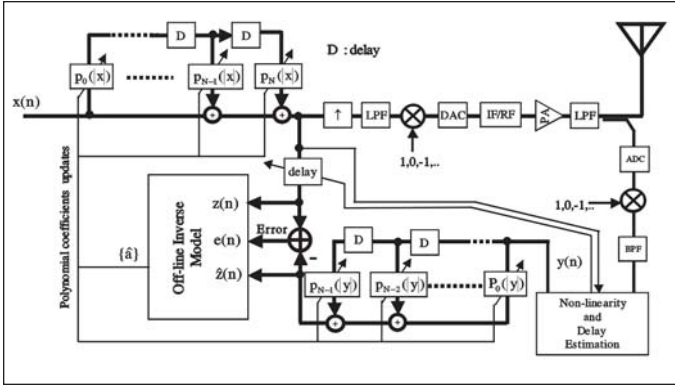


Figure 1 · Hardware implementation of offline inverse PA linearization.

real time. Thus, the offline computation can be carried out in blocks of samples over short durations that are distanced in a long interval (typically minutes), which makes this approach very attractive, expensive, and upgradable.

To illustrate the principle of linearization based on the system in Figure 1 mathematically, consider the mathematical equivalent model illustrated in Figure 2. Here the online pre-distorter (running in real time) and the PA offline model (running at predetermined intervals using only short block of samples) are implemented using eqs. (1) and (2). The task is then for the off-line model to iteratively minimize the error signal given by $z(n) - \hat{z}(n)$ where $\hat{z}(n)$ is the offline estimate of the pre-distorter actual output given by,

$$\hat{z}(n) = x(n)(G_{PD}H_{PD})(G_{PA}H_{PA})(G_{BB-RF}G_{RF-BB})(G_M H_M) \quad (3)$$

where G indicates constant linear gain,

$$H_M = H_M(|x(n)|)$$

and

$$H_{PA} = H_{PA}(|y(n)|)$$

indicates the discrete non-linear function operating on the input and output of the PA respectively, and n is the sample number, where the actual reference signal $z(n)$ in (3) is given by,

$$z(n) = x(n)(G_{PD}H_{PD})$$

The iterative on-line coefficients identification algorithm would then converge when $z = \hat{z}(n)$, resulting in

$$G_{RF-BB}G_{PA}G_M H_{PA}H_M = 1 \quad (4)$$

If the overall baseband gain in (4) is designed to be

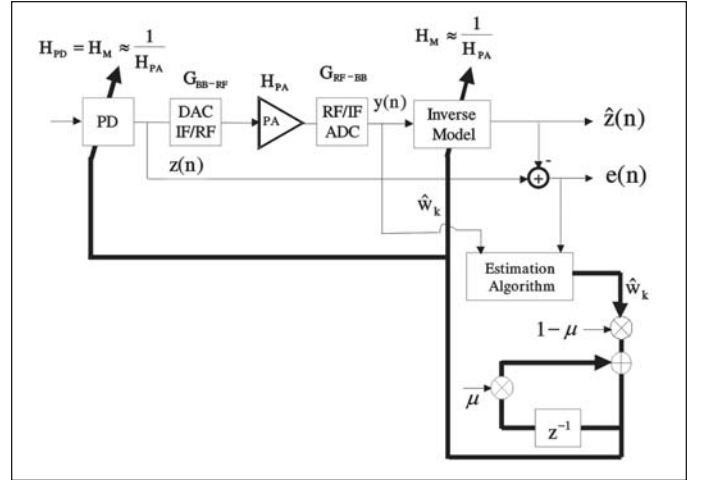


Figure 2 · PA inverse polynomial estimation using offline system identification.

equal to the gain of the PA, that is $G_{BB-RF}G_{RF-BB} = 1/G_{PA}$, the off-line PA model would then approximate the PA actual inverse, that is (4) would yield

$$H_{PA}^{-1}(|y|) = G_M H_M(|x|)$$

Adaptive Coefficients Estimation

As detailed earlier, the off-line algorithm illustrated in Figure 1 and Figure 2 are obtained iteratively using blocks of captured baseband input and outputs of the PA. There are a number of methods that can be used for the iterative weights estimate of a_{mk} in (2), however, least-square estimation algorithms are preferred for their performance and reasonable complexity. The off-line coefficients in Figure 1, $\mathbf{w} = \{a_{mk}\}$, are first initialized to all zeros except the linear terms of (2), then the inverse required estimator output given by $\hat{z}(n) = \mathbf{w}^T y(n)$ obtained, where T is the transpose of a complex matrix. The modelling error $z(n) - \hat{z}(n)$ is then extracted and used to solve for the next set of weights of (2). This error measure is given by,

$$E\{e^2(n)\} = E\{(z(n) - \mathbf{w}^T y(n))^2\} \quad (5)$$

Expanding (5), further yields,

$$E\{e^2(n)\} = E\{z^2(n)\} - 2\mathbf{w}^T E\{z(n)y(n)\} + \mathbf{w}^T E\{y(n)y^T(n)\}\mathbf{w} \quad (6)$$

By inspection of (6), it is revealed that the MSE contains both PA input-output signal cross-correlation matrix \mathbf{P} and the PA output signal autocorrelation matrix \mathbf{R} .

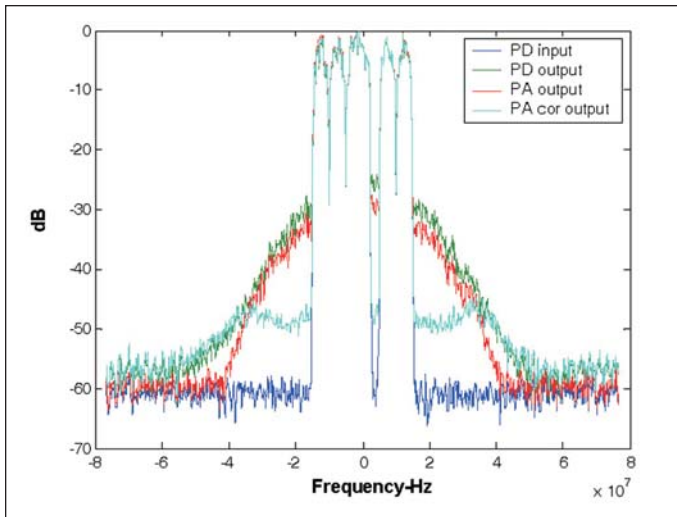


Figure 3 · Simulated illustration of linearization performance using RLS estimator.

Borrowing terminology from [6], the MSE is then given by,

$$E\{e^2(n)\} = \sigma_d^2 - 2\mathbf{w}^T \mathbf{P} + \mathbf{w}^T \mathbf{R} \mathbf{w} \quad (6a)$$

The optimal coefficients $\hat{\mathbf{w}}$ are then obtained by setting the derivative of the MSE in (6) with respect to the weights to zero

$$\frac{d}{d\mathbf{w}} E\{e^2(n)\} = 0$$

which produces the well known Weiner solution given by $\hat{\mathbf{w}} = \mathbf{R}^{-1} \mathbf{P}$. Clearly, to obtain this a large complex matrix inversion has to take a place \mathbf{R}^{-1} that has dimensions of KL , where L is the memory depth, and K is the polynomi-

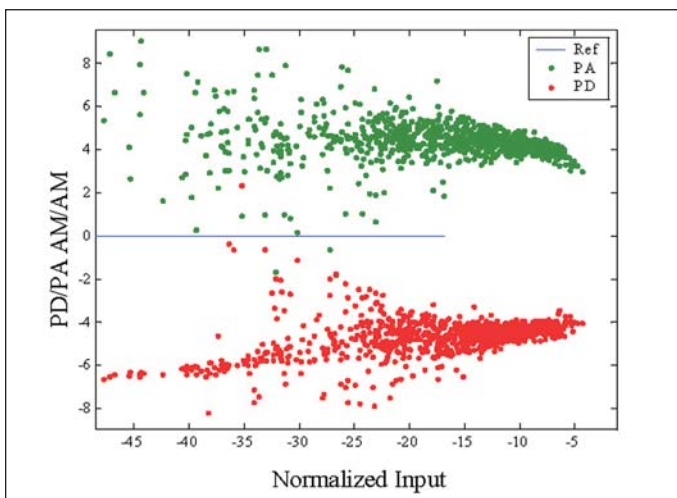


Figure 4 · The measured PD/PA AM/AM transfer functions.

al order of (2). Fortunately, several iterative algorithms that have lower complexity and do not involve inverting a matrix exists and are available, such as the least mean square (LMS) and recur-sive least square (RLS) [6]. There are also a number of other approaches to obtaining the weights iteratively [7, 8, 9]; among these are the exploitation of neural nets and genetic algorithms, the use of Volterra series, and the use of Hammerstein system. After investigating these algorithms, it was determined that the RLS algorithm seems to be the preferred method due to its accuracy, stability, and fast convergence. The LMS algorithm has very slow converging and noisy weight estimates, but in some scenarios it was able to sustain some disturbances that the RLS could not cope with. This was mainly traced to the instances, where the PA input signal was no longer considered stationary.

Simulated Performance

Using computer simulations, a RLS-based algorithm im-plementation as illustrated in Figure 3 was first simulated with the PA input being three WCDMA-like channels as illustrated in Figure 2. The offline model and on-line pre-distorter were implemented using (1) and (2) respectively with three taps ($L = 3$) and a 7th order polynomial ($K = 7$). Figure 3 illustrates the spectrum of the PA and pre-distorter signals before and after linearization after 8000 iterations using a sampling frequency of 153 Msp/s. The figures shows that correction is in the order of ~ 27 dB, which was a very optimistic figure, but explainable when considering that the forward and reverse paths were ideal with perfect timing alignment, high transmitter SNR, and the algorithm was implemented using floating point mathematics.

Experimental Performance

The system in Figure 1 was implanted to linearize a

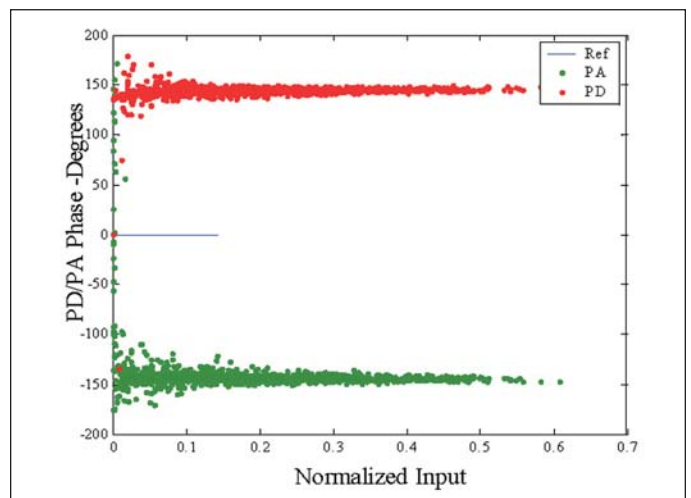


Figure 5 · The measured PD/PA AM/PM transfer functions.

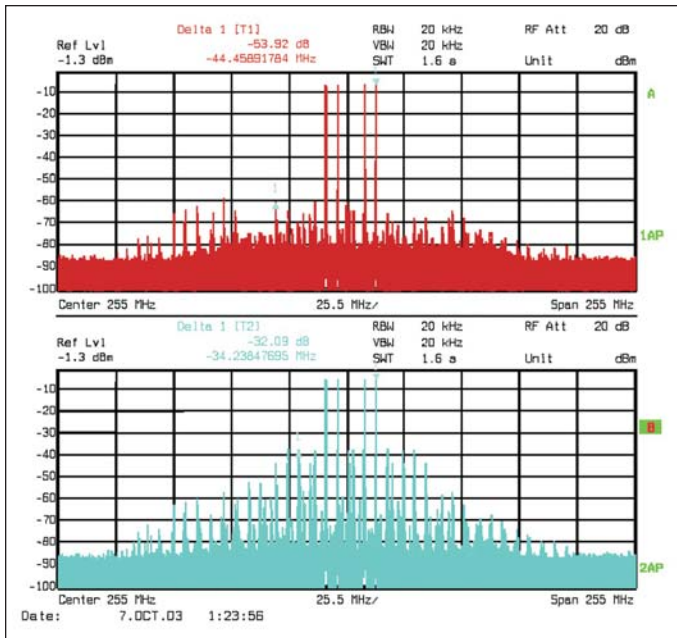


Figure 6 · Measured overall linearization performance.

power amplifier with a moderate memory and an average output power of 41 dBm. Figure 4 and Figure 5 illustrate the measured complex transfer functions of the PA and PD captured in the digital domain before and after linearization, respectively. It is observed how the transfer function of the PD is an approximate inverse of the PA transfer function in both figures.

Figure 6 shows the measured spectrum of the PA output signal before and after linearization, where the correction was close to 22 dB over the bandwidth of 10 MHz. Figure 7 shows a snap shot of the measured RLS algorithm adaptive weight estimates for the first tap of (2). It is observed that the algorithm converges in less than 1000 samples, where the sampling rate was 150 Msps.

One way to validate the RLS weights estimates in Figure 2 during various iterations is to observe the signals of $\hat{z}(n)$, $z(n)$, and the mean square error signal as illustrated in Figure 8. Here the MSE (top graph) is computed as

$$20\log_{10}(|z(n) - \hat{z}(n)| / |z(n)|)$$

while the bottom figure shows the estimated versus actual pre-distorter signal, indicating very rapid convergence for the case of the RLS with forgetting factor of 1 [6].

Conclusion

Using theoretical analysis, computer simulations, and hardware prototyping, this paper has illustrated the use of inverse modeling in identifying and linearizing a non-memory-less RF amplifier. It is evident from the experimental results, that linearization improvement of more

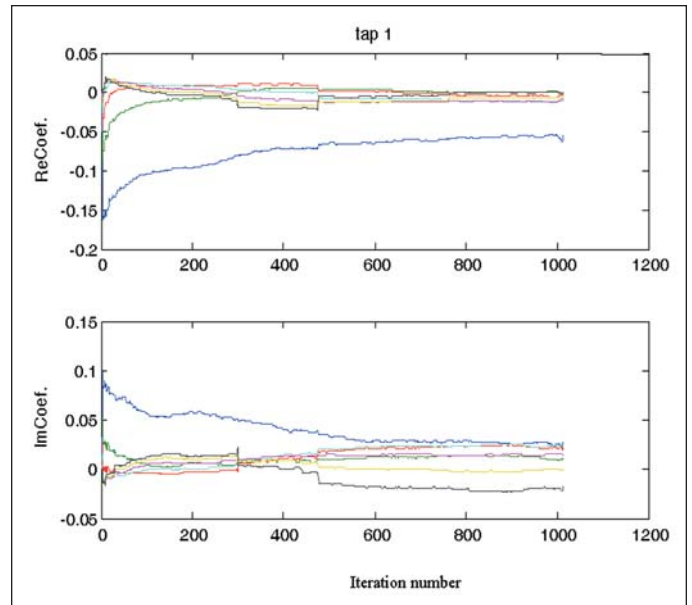


Figure 7 · Measured coefficients of the first pre-distorter tap.

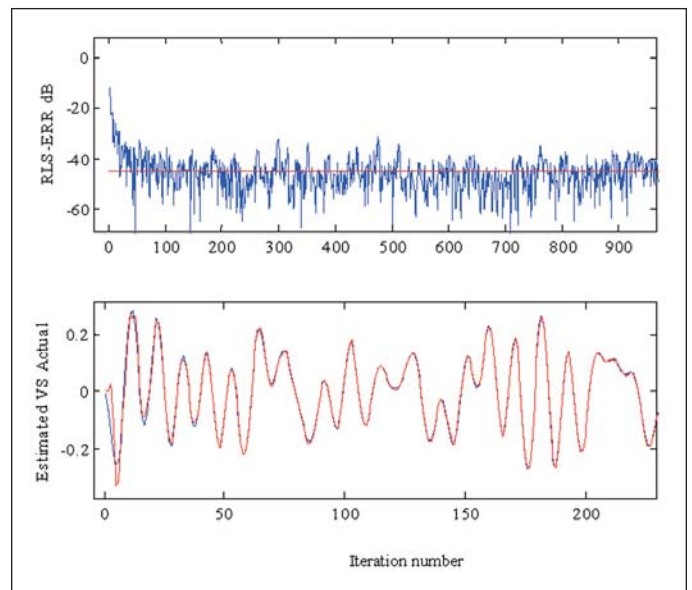


Figure 8 · The adaptive algorithm error signal (top), and a comparison between the actual and the off-line estimated PD output.

than 15 dB can be obtained over a bandwidth that is suitable for third and fourth generation broadband wireless services.

References

1. S. C. Cripps, *RF Power Amplifiers for Wireless Communications*, Artech House, Norwood, MA, 1999.

2. J. S. Kenney, W. Woo, L. Ding, R. Raich, H. Ku, and G. T. Zhou, "The impact of memory effects on predistortion linearization of RF power amplifiers," *Proc. 8th Int. Microwave Opt. Technol. Symp.*, Montreal, QC, Canada, June 19-23, 2001, pp. 189-193.

3. M. McBeath, D. T. Pincley, and J. R. Cruz, "W-CDMA Power Amplifier Modeling," Vehicular Technology Conference, 2001. *VTC 2001*, Volume: 4, 7-11 Oct., pp: 2243 - 2247.

4. Muhammad A. Nizamuddin, P. Balister, W. Trenter, and J. Reed, "Nonlinear tapped delay line digital pre-distorter for power amplifier with memory," *Wireless Communications and Networking, 2003*, Vol. 1, pp: 607 - 611.

5. Ding, L., Zhou, G.T., Morgan, D.R., Ma, Z., Kenney, J.S., Kim, J., Giardina, C.R., "A Robust Digital Baseband Predistorter Constructed Using Memory Polynomials," *IEEE Trans. Comm.*, Vol. 52, No. 1, Process pp. 159-165, January, 2004.

6. Simon Haykin, *Adaptive Filter Theory*, Prentice Hall, 2002.

7. Hua Qian and G. Tong Zhou, "A Neural Network Pre-distorter For Nonlinear Power Amplifiers With Memory," *Digital Signal Processing Workshop*, 2002, pp: 312 - 316.

8. C. Eun and E. J. Powers, "A new Volterra pre-distorter based on the indirect learning architecture," *IEEE Trans. Signal Process.*, pp. 223-227, Jan. 1997.

9. L. Ding, R. Raich, and G.T. Zhou, "A Hammerstein pre-distorter design based on indirect learning architecture," *Proc. IEEE Intl. Conf. Acoust. Speech Sig. Process.*, 2002.

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Dr. Nezami has taught as an adjunct professor and worked as a researcher and developer of electronic systems in USA for 15 years. Until 2004, Dr. Nezami was a Sr. Principal Design Engineer with the Raytheon Company in St. Petersburg Florida-

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