An Overview of Common Techniques for Power Amplifier Linearization

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Here is a brief review of the major techniques for improving power amplifier linearity, with an extensive reading list for additional study on topics of interest The subject of power amplifier linearization is much more complex than a basic tutorial article can cover. However, this note will serve as a reminder of the primary methods used to

enhance linearity. Also, we have provided an extensive Bibliography that can be used for further research on particular methods.

Important Methods for Linearization

Class A operation—Biasing a power device for conduction in its linear region over the entire swing of the input signal waveform provides linear operation, but with low efficiency. When power consumption, size, weight and thermal considerations can be accommodated, a class A amplifier can be an effective solution.

Feedback—Sampling a portion of an amplifier's inverted waveform output, then summing it with the input, partially cancels distortion products. Gain is reduced, and the time delay introduced by the feedback signal path may limit the upper frequency range.

Feedforward methods—Like feedback, there is a cancellation of the distorted output signal, but without the time delay of re-introducing an output sample at the input. Instead, there are two signal paths, one of which is highly linear, and carries a sample of the undistorted input signal. This signal is compared with a sample of the main signal path output, resulting in an error signal consisting (ideally) of only distortion products. This error signal is inverted and summed with the output signal, cancelling some of the distortion products. Delays are introduced to match the



Figure 1 · Block diagram of the cross-cancellation technique.

main signal and the error channels. There are numerous methods and topologies for acquiring the output sample, taking the difference with the undistorted signal, and recombing the error signal with the distorted output.

Cross-cancellation—This might be considered a hybrid of feedback and feedforward techniques. The input signal is divided, then amplified by two identical power amplifiers. Feedback is obtained from the output of one amplifer, but introduced to the input of the opposite amplifier. Because the sampled and corrected signal paths are separate, delays can be introduced at the input of one and the output of the other, eliminating the time difference between the through and feedback signals (see Fig. 1).

Analog Predistortion—Linearity can be improved in some systems by implementing a piecewise approximation of an amplitude transfer function that is opposite that of the nonlinear amplifying device. The degree of improvement is limited by the accuracy and stability of the circuitry.

Digital predistortion—A sample of the output can be routed to a fast signal processor

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that calculates the necessary correction to the transfer function. Speed must be sufficient to update the correction as operating parameters change due to thermal effects, aging, duty cycle and output power level. This method is being extensively used, and steadily improved, in current wireless systems.

Linearity by design—There are also amplifier circuits such as the Doherty method, outphasing, and combined polar/amplitude modulation that have greater inherent linearity, at the cost of increased complexity. These are not linearization techniques per se, but do result in an amplifier with greater linearity than traditional designs.

Bibliography-Textbooks

S. Cripps, *RF Power Amplifiers for Wireless Communications*, 2nd ed., Artech House, 2006.

P. B. Kenington, *High Linearity RF Amplifier Design*, Boston: Artech House, 2000.

J.L.B. Walker, *High-power GaAs FET Amplifiers*, Artech House, 1999.

N. Pothecary, *Feedforward Linear Power Amplifiers*, Artech House, 1999.

Papers and Articles

R. Gutierrez, "The RFAL Technique for Cancellation of Distortion in Power Amplifiers," *High Frequency Electronics*, vol. 4, pp. 18-28, June 2004.

R. Guutierrez, "High-Efficiency Linearized LDMOS Amplifiers Utilize the RFAL Architecture," *High Frequency Electronics*, Feb. 2006.

R. Gupta, S. Ahmad, R. Ludwig, J. McNeill, "Adaptive Digital Baseband Predistortion for RF Power Amplifier Linearization," *High Frequency Electronics*, Sep. 2006.

B. Lee, L. Dunleavy, "Understanding Base Biasing Influence on the Large Signal Behavior in HBTs," *High Frequency Electronics*, May 2007.

S. Wood, R. Pengelly, J. Crescenzi, "A High Efficiency Doherty Amplifier with Digital Predistortion for WiMAX," *High Frequency Electronics*, Dec. 2008.

T. Nelson, "Intergrated Receiver Simplifies the Analog Side of Digital Predistortion, *High Frequency Electronics*, July 2009.

A. Grebennikov, "Linearity Improvement Techniques for Wireless Transmitters: Part 1," *High Frequency Electronics*, May 2009.

A. Grebennikov, "Linearity Improvement Techniques for Wireless Transmitters: Part 2," *High Frequency Electronics*, June 2009.

S. Wood, R. Pengelly, D. Farrell, C. Platis, J. Crescenzi, "High-Power, High Efficiency GaN HEMT Power Amplifiers for 4G Applications," *High Frequency Electronics*, May 2009.

H. Choi, Y. Jeong, J. S. Kenney, and C. D. Kim, "Cross Cancellation Technique Employing an Error Amplifier," *IEEE Microwave and Wireless Comp. Lett.*, vol. 18, July 2008.

M. Nakayama, K. Mori, K. Yamauchi, Y. Itoh, and Y. Mitsui, "An Amplitude and Phase Linearizing Technique for Linear Power Amplifiers," *Microwave Journal*, vol. 39, Mar. 1996.

J. Yi, Y. Yang, M. Park, W. Kang, and B. Kim, "Analog Predistortion Linearizer for High-Power RF Amplifiers," *IEEE Trans. Microwave Theory Tech.*, vol. 48, Dec. 2000.

I. Kim, J. Cha, S. Hong, Y. Y. Woo, J. Kim and B. Kim, "Predistortion Power Amplifier for Base-Station Using a Feedforward Loop Linearizer," *Proc. 36th Europ. Microwave Conf.*, 2006.

A. R. Mansell and A. Bateman, "Adaptive Predistortion with Reduced Feedback Complexity," *Electronics Letters*, vol. 32, June 1996.

H. S. Black, "Stabilized Feed-Back Amplifiers," Proc. IEEE, vol. 87, Feb. 1999: reprinted from *Electronic* Engineering, vol. 53, Jan. 1934.

H. S. Black, "Inventing the Negative Feedback Amplifier," IEEE Spectrum, vol. 14, Dec. 1977.

H. A. Rosen and A. T. Owens, "Power Amplifier Linearity Studies for SSB Transmissions," *IEEE Trans. Commun. Syst.*, vol. CS-12, June 1964.

C.-C. Hsieh and E. Strid, "A S-Band High Power Feedback Amplifier," 1977 IEEE MTT-S Int. Microwave Symp. Dig.

S. Narayanan, "Application of Volterra Series to Intermodulation Distortion Analysis of Transistor Feedback Amplifiers," *IEEE Trans. Circuit Theory*, vol. CT-17, Nov. 1970.

Y. Kim, Y. Yang, S. Kang, and B. Kim, "Linearization of 1.85 GHz Amplifier Using Feedback Predistortion Loop," 1998 IEEE MTT-S Int. Microwave Symp. Dig.

Y. Y. Woo, J. Kim, J. Yi, S. Hong, I. Kim, J. Moon, and B. Kim, "Adaptive Digital Feedback Predistortion Technique for Linearizing Power Amplifiers," *IEEE Trans. Microwave Theory Tech.*, vol. MTT-55, May 2007.

T. Arthanayake and H. B. Wood, "Linear Amplification Using Envelope Feedback," *Electronics Lett.*, vol. 7, Apr. 1971.

J.-S. Cardinal and F. Ghannouchi, "A New Adaptive Double Envelope Feedback (ADEF) Linearizer for Solid State Power Amplifiers," *IEEE Trans. Microwave Theory Tech.*, vol. MTT-43, July 1995.

V. Petrovic and W. Gosling, "Polar-Loop Transmitter," *Electronics Lett.*, vol. 15, May 1979.

V. Petrovic, "Reduction of Spurious Emission from Radio Transmitters by Means of Modulation Feedback," *Proc. IEE Conf. Radio Spectrum Conservation Techn.*, 1983.

M. Johansson and L. Sundstrom, "Linearisation of RF Multicarrier Amplifiers Using Cartesian Feedback," *Electronics Lett.*, vol. 30, July1994.